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Basic FPGA Architecture; Xilinx Tool Flow; Lab 1: Xilinx Tool Flow. An introduction to FPGA design flow. Open a project containing the PicoBlaze 8-bit microcontroller and simulate the design using the ISim HDL simulator provided with the ISE Foundation software. Architecture Wizard and Pins Assignment; Lab 2: Architecture Wizard and Pins Assignment

FPGA Design Flow using ISE - Xilinx

Xilinx has acquired the assets of FPGA compiler developer Falcon Computing Solutions in the US. Falcon's high-level synthesis (HLS) compiler

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optimisation technology will make adaptive computing more accessible to software developers as part of the Xilinx Vitis unified software platform with automated hardware-aware optimizations.

Xilinx acquires Falcon Computing FPGA compiler technology

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